Rewrite Rules supporting Conditional Statements for Automatic Parallelization

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ABSTRACT
Hurlin recently proposed a technique for automatic parallelization based on separation logic [9]. This paper proposes an enhancement of the mechanism for situations where conditional statements are used in specifications of programs. With the proposed changes, specifications containing conditional statements are supported and programs can be automatically parallelized. Two approaches are identified and a preferable one is suggested. The proposed extension is illustrated by an exemplifying program and has been implemented.

Keywords
Proof Rewriting, Separation Logic, Automatic Parallelization and Optimization, Conditional Hoare Triples

1. INTRODUCTION
Current trend of multi-core systems and computational needs have led to the development of parallel systems. Parallel architectures are designed to run on multi-core systems as well as across network applications. Since manually writing parallel systems is much harder than writing sequential systems, automatic parallelization might provide effective and sound speedups [4, 1, 9].

Several techniques are proposed for automatic parallelization [7, 10, 6]. These techniques use pointer analysis to prove that if mutations on data done by a part of a program do not interfere with other parts of the program, these blocks can run in parallel. Once it is known which part of the data is accessed and which part is not, parallelization can be done in an automatic manner.

Hurlin proposes a new approach to automatic parallelization that uses separation logic to detect disjoint access to the heap [9].

This paper proposes an enhancement of the mechanism to support parallelization when conditional statements are used in specifications.

The following sections are outlined as follows: the mechanism of automatic parallelization using separation logic is presented in section 2, the limitations of this mechanism as well as using conditional statements is described in section 3, an extension to the mechanism is proposed in section 4 and a conclusion is drawn in section 5.

2. BACKGROUND INFORMATION
The behavior of a program can be specified by pre- and postconditions [8]. Pre- and postconditions are formulas describing the heap and store at method entry and exit. The heap and the store describe the data structure of a program. To check whether the pre- and postconditions are valid, they should be verified against the actual code.

Smallfoot is a framework used for automatic verification of an annotated programs written in Smallfoot’s language [2, 3]. Smallfoot’s verifier takes an annotated (i.e. pre- and postconditions) program and returns, if this program is correct, a proof. The annotations are written in separation logic, the proof is constructed using Hoare rules.

A correct program is described by a Hoare triple, written as:

\[ \{X\}C(\Theta) \]

where and are formulas and is a program. Such a triple states that if starts with a heap described by the precondition and, if terminates, the resulting heap is described by postcondition [8].

The proof of a verified program can be transformed by rewrite rules. These rules induce local changes, which have no effect on specifications, but change the code. Because the proof of a verified program consists of Hoare triples, the proof include both annotations as well as actual commands. By rewriting parts of the proof the code of the program can be changed [9].

In other words a verified program can be rewritten in such a way that its pre- and postcondition remain valid, but the code itself has changed. In this way code can be parallelized.

2.1 Heaps
Heaps and stores describe the data structure of a program.

Heaps are partial functions from pointers (Loc) to finite collections (Fields) of non-addressable values (Val) and pointers. Stores are total functions from variables (Var) to non-addressable values and pointers (see figure 1) [3].

\[
\begin{align*}
\text{Heaps} & \overset{\text{def}}{=} \text{Loc} \rightarrow (\text{Fields} \rightarrow \text{Val} \cup \text{Loc}) \\
\text{Stores} & \overset{\text{def}}{=} \text{Var} \rightarrow \text{Val} \cup \text{Loc}
\end{align*}
\]

Figure 1: Definition of the heap

For example, consider a store \( s \) that maps variables \( x \) and \( y \) to address 42 and 43:

\[ s \overset{\text{def}}{=} x \mapsto 42, y \mapsto 43 \]

Then, consider a heap \( h \) that maps fields \( f \) and \( g \) of the cell at address 42 to 3 and 5 and maps fields \( f \) and \( g \) of the cell at address 43 to 1 and 2:

Figure 3 depicts heap \( h \) and store \( s \) graphically.
\[ h \equiv 42 \mapsto \begin{pmatrix} f \mapsto 3 \\ g \mapsto 5 \end{pmatrix}, \quad 43 \mapsto \begin{pmatrix} f \mapsto 1 \\ g \mapsto 2 \end{pmatrix} \]

Figure 2: Heap \( h \) at address 42 and 43

\[
\begin{array}{c|c}
\text{X} & 3 \\
\hline
\text{Y} & 1 \\
\end{array}
\]

(a) \( h' : x \mapsto 42 \)

(b) \( h'' : y \mapsto 43 \)

Figure 3: Heap \( h = h' \cup h'' \) and store \( s \)

### 2.2 Separation Logic

In separation logic, formulas describe the heap [12]. This relation between formulas and heaps is given by the semantics of formulas, i.e. by \( \varepsilon \)'s definition (see figure 4). The syntax of formulas is given in figure 7.

\[
\begin{align*}
\llbracket s \rrbracket s &\overset{def}{=} s(s) \quad \llbracket \text{nil} \rrbracket s \overset{def}{=} \text{nil} \\
\text{if } s, h \equiv \Pi_0 &\cap \Pi_1 \overset{def}{=} s, h \equiv \Pi_0 \land s, h \equiv \Pi_1 \\
\text{if } s, h \equiv E_{\rightarrow} &\equiv [f_1 : E_1, \ldots, f_n : E_n] \overset{def}{=} h \equiv [E_0]s \equiv r \text{ where } r(f_i) = [E_i]s \text{ for } i \in 1..k \\
\text{if } s, h \equiv \text{emp} &\overset{def}{=} h = \emptyset \\
\text{if } s, h \equiv \Sigma_2 &\equiv \Sigma_1 \overset{def}{=} \exists h_0, h = h_0 \star h_1 \text{ and } s, h_0 \equiv \Sigma_2 \text{ and } s, h_1 \equiv \Sigma_1 \\
\text{if } s, h \equiv \Pi \cap \Sigma &\overset{def}{=} s, h \equiv \Pi \text{ and } s, h \equiv \Sigma
\end{align*}
\]

Figure 4: Semantics of formulas

Top-level formulas \( \Sigma \) and \( \Theta \) are pairs of a pure formula \( \Pi \) and a spatial formula \( \Sigma \). \( \Pi \) represents equality and inequality of expressions (e.g. \( E \neq \text{nil} \)). Multiple pure formulas are combined using the \( \land \)-operator.

Spatial formulas \( \Sigma \) represent facts about the heap. A simple (\( S \)) spatial formula: \( E \mapsto [\rho] \) represents a heap with one cell at address \( E \) and content \( \rho \). This is formalized in the semantics by imposing \( h = [E \mapsto \rho] \). Here \( \rho \) is a sequence of adjacent fields.

To exemplify the relation between heaps and formulas, consider figure 2 and the following formulas:

\[
\begin{align*}
X &= x \mapsto [f : 3, g : 5] = x \mapsto 3, 5 \\
\Theta &= y \mapsto [f : 1, g : 2] = y \mapsto 1, 2
\end{align*}
\]

Now the relation is given by the semantics (where \( h \) is \( h' \cup h'' \)):

\[
\begin{align*}
s, h' &\equiv X \quad \text{and} \quad s, h'' \equiv \Theta
\end{align*}
\]

The logical operation \( X \star \Theta \) asserts that \( X \) and \( \Theta \) hold for disjoint portions of the addressable heap. Names for this operator are separating conjunction, independent or spatial conjunction. \( X \star \Theta \) enforces \( X \) to be disjoint to \( \Theta \): no heap cells found in \( X \) are also in \( \Theta \) [3].

To illustrate the \( \star \)-operator, consider a heap \( h \) and store \( s \): \( s, h \equiv X \) and \( s, h \equiv \Theta \) where \( X = x \mapsto 3, 5 \) and \( \Theta = y \mapsto 3, 5 \):

\[
\begin{align*}
h &\equiv \begin{pmatrix} f \mapsto 3 \\ g \mapsto 5 \end{pmatrix}, \quad A' \equiv \begin{pmatrix} f \mapsto 3 \\ g \mapsto 5 \end{pmatrix}
\end{align*}
\]

In this example two cases are possible depending on the equality of \( A \) and \( A' \):

\[
\begin{align*}
s, h \equiv X \star \Theta &\quad : \quad A \text{ is not equal to } A' \quad \text{figure 5(a)} \\
s, h \equiv X \land s, h \equiv \Theta &\quad : \quad A \text{ is equal to } A' \quad \text{figure 5(b)}
\end{align*}
\]

\[
\begin{array}{c|c}
\text{X} & 3 \\
\hline
\text{Y} & 3 \\
\hline
\end{array}
\]

(a) \( s, h \equiv X \star \Theta \)

(b) \( s, h \equiv X \land s, h \equiv \Theta \)

Figure 5: A more global view of the heap

Simple spatial formulas \( S \) include the tree predicate \( \text{tree}(E) \), which is defined by an equivalence (see figure 6). This equivalence states that an expression is a tree when it’s either \( \text{nil} \) or it has two children as trees. Thus \( E = \text{nil} \lor \text{emp} \) can also be written as \( \text{tree}(E) \).

\[
\text{tree}(r) \equiv (r = \text{nil} \lor \text{emp})^\lor
\]

(\( \forall \lceil r \rceil \rightarrow l : l : r \star \text{tree}(l) \star \text{tree}(r) \))

Figure 6: Tree predicate and its equivalence

\[
\begin{align*}
x, y, z &\in \text{Var} \quad \text{variables} \\
E, F, G &::= \text{nil} \mid x \quad \text{expressions} \\
b &::= E \lor E \mid E \neq E \quad \text{boolean expressions} \\
\Pi &::= b \mid \Pi \land \Pi \quad \text{pure formulas} \\
f, g, f_1, f_2, \ldots &\in \text{Fields} \quad \text{fields} \\
\rho &::= f_1 : E_1, \ldots, f_n : E_n \quad \text{record expressions} \\
S &::= E \mapsto [\rho] \mid \text{tree}(E) \quad \text{simple spatial} \\
&\quad \text{if } \Pi \text{ then } S \lor \Theta \quad \text{formulas} \\
\Sigma &::= \text{emp} \mid S \mid \text{tree}(E) \quad \text{spatial formulas} \\
\Xi, \Theta, X, \zeta, \Phi &\in \Pi \mid \Sigma \quad \text{formulas}
\end{align*}
\]

Figure 7: Syntax of formulas

### 2.3 Hoare Logic

Hoare logic describes the behavior of programs using triples and rules.

Hoare triples are written \( \{X \} C \{\Theta\} \) where \( X \) and \( \Theta \) are formulas and \( C \) is a program. Such a triple states that if program \( C \) starts with a heap described by the precondition \( X \), and if \( C \) terminates, the resulting heap is described by the postcondition \( \Theta \) [8].

An example of a triple is: \( \{x < N\} : x := 2 \cdot x \times (x < 2 \cdot N) \).

Triples for sequential programs are built using the sequence rule (see figure 8), the lower premise can be deduced from the upper ones. This rule means that if two Hoare triples describe a pair of adjacent commands, such that the first triple’s postcondition is equal to the second triple’s precondition (\( \Xi' \)), then these triples can be combined in a single Hoare triple.

#### 2.3.1 Frame Rule
The frame rule (see figure 9) extends the local assertions about the heap to global specifications by separating the heap by using separating conjunction ($\star$). This rule means that the part of the heap described by $\Theta$ is untouched by $C$’s execution [11].

$$\frac{(\Xi \land C[\Xi'])}{(\Xi \star \Theta)[C[\Xi'] \star \Theta]}$$  Frame

Figure 9: Frame rule

As an example, consider a program $C$ that alters the heap by changing the first cell (from 3 to 8) and adding a cell (2 after 5). $C$’s precondition is described by $\Xi$, and $C$’s postcondition is described by $\zeta$:

$$\Xi = x \mapsto 3, 5 \quad \zeta = x \mapsto 8, 5, 2$$

Using the frame rule $C$’s pre- and postcondition ($\Xi$ and $\zeta$) can be extended to ($\Xi \star \Theta$ and $\zeta \star \Theta$):

$$\Theta = y \mapsto 3, 5$$

$$\frac{(\Xi \star \Theta)[\zeta]}{(\Xi \star \Theta)[\zeta \star \Theta]}$$  Frame

$\Theta$ is disjoint to $\Xi$ and $\zeta$, therefore the heap described by $\Theta$ is not accessed by $C$’s execution (see figure 10).

\begin{align*}
X &\mapsto 3 & \quad X &\mapsto 8 \\
\quad &\mapsto 5 & \quad &
\end{align*}

\begin{align*}
Y &\mapsto 3 & \quad Y &\mapsto 2 \\
\quad &\mapsto 5 & \quad &
\end{align*}

(a) $\Xi \star \Theta$  \quad (b) $\zeta \star \Theta$

Figure 10: $C$ is framed by $\Theta$

2.3.2 Parallel Rule

Figure 11 shows the Hoare rule for parallel statements. This rule shows that if two processes ($C$ and $C'$) access disjoint parts of the heap ($\Xi$ and $\Xi'$) and ($\Theta$ and $\Theta'$), they can execute in parallel [12].

$$\frac{(\Xi \land C[\Xi'])}{(\Xi \star \Xi')[C[C'] \star \Theta \star \Theta']}$$  Parallel

Figure 11: Parallel rule

As an example, figure 12 illustrates an update of two disjoint fields in parallel.

2.4 Smallfoot

Smallfoot is a framework used for automatic verification of a specified program [2, 3]. Smallfoot consists of three parts: 1) a language, shown in section 2.4.1; 2) a set of formulas that is used to annotate programs written in this language, shown in section 2.2; and 3) a set of Hoare rules to verify that annotated programs are correct. Some of these rules are shown in section 2.3.

A Smallfoot program is specified as shown in figure 13; where $p$ is the program name; $\Xi$ and $\Theta$ are formulas describing the heap; $\Xi$ is the precondition; $\Theta$ is the postcondition; $E_1$ and $E_2$ are a comma-separated group of expressions (the former is passed by reference, the latter by value); and $C$ is the actual program (written in the Smallfoot language).

2.4.1 Smallfoot language

Smallfoot’s language specification is shown in figure 14.

\begin{align*}
&\quad r \in Resources \\
&\quad x, y, z \in Var \\
&\quad E, F, G ::= nil \mid x \\
&\quad b ::= E = E \mid E \neq E \\
&\quad f, g, f_1, f_2, \ldots \in Fields \\
&\quad A ::= x := E \mid x := E \rightarrow f \mid E \rightarrow f := F \\
&\quad \mid x := \text{new}() \mid \text{dispose}(E) \\
&\quad C ::= A \mid \text{empty} \mid \text{if } b \text{ then } C \text{ else } C' \\
&\quad \mid \text{while}(b)(C) \mid \text{lock}(r) \mid \text{unlock}(r) \\
&\quad \mid p(E_1; E_2) \mid C \mid C[C']
\end{align*}

Figure 14: Smallfoot’s language specification

The language specifies several atomic commands $A$: $x := E$ makes $x$ an alias of $E$; $x := E \rightarrow f$ assigns the content of field $f$ at address $E$ to $x$; $E \rightarrow f := F$ makes the content of field $f$ at address $E$ to $F$; $x := \text{new}()$ assigns the address of a new cell to $x$; and $\text{dispose}(E)$ disposes the cell at address $E$.

Other commands $C$ are: conditionals if $b$ then $C$ else $C'$; procedure calls $p(E_1; E_2)$; sequential execution $C$; $C$ concurrent execution $C[C']$; loops while($b$)($C$); and resources acqurement and releasing $\text{lock}(r) \mid \text{unlock}(r)$.

Smallfoot’s resources sharing mechanism goes beyond the scope of this paper, see Berdine et al. for further references [3].

2.4.2 Verification mechanism

The Smallfoot verifier has information about how the heap is changed by each atomic command, and it tries to compose a proof using Hoare rules. The atomic commands are described by the leaves of a proof tree and the top level annotation is described by the root.

Intuitively, Smallfoot’s verifier has a Hoare rule for each atomic and non-atomic command (see figure 14). Hoare triples for commands are then combined by using the sequence rule (see figure 8) [2, 3].

If a program can be verified, a proof tree is generated, as the next section exemplifies.
2.4.3 Shallow

Shallow is a small program written in the Smallfoot language and shall be used for illustrative purposes (see figure 15). The program takes three trees \( (\text{tree}(t), \text{tree}(p), \text{tree}(q)) \) and an expression \( (d) \) as input. This is formalized by Shallow’s pre and postcondition (enclosed in square brackets)\(^1\).

Shallow updates \( \text{tree}(p) \) and \( \text{tree}(q) \) using the method update (see figure 16). The recursive method mirror (see figure 17) disposes nodes in tree \( \text{tree}(t) \) lower than depth \( d \) and mirrors the resulting subtree (see figure 18).

The correctness of Shallow is described in figure 26 (see appendix B, upper tree). The tree shows applications of Hoare rules: frame and sequence (the or-rule will be explained in section 3). The root node of the proof tree is equal to the specification (see frame and sequence (the or-rule will be explained in section 3)). The correctness of Shallow is described in figure 26 (see appendix B, upper tree). The tree shows applications of Hoare rules: frame and sequence (the or-rule will be explained in section 3).

The proof of a verified program can be transformed by rewrite rules. These rules induce local changes, but have no effect on the code’s specification. Because the proof of a verified program consists of Hoare triples, the proof includes both annotations as well as actual commands. By rewriting parts of the proof the program’s code can be changed.

The whole procedure is as follows: given a program \( C \), \( C \) is proven using Smallfoot, a proof tree \( P \) is generated if \( C \) can be verified. Then, \( P, C \) is rewritten into \( P, C' \), such that \( P' \) is a proof of \( C' \) and \( C' \) is a parallelized and optimized version of \( C \).

A rewrite rule will be explained in section 2.5.2.

2.5 Proof Rewriting

Proof trees of verified programs contain information about 1) parts of the heap which are accessed by commands, called anti-frames; and 2) parts of the heap which are not touched, called frames [5].

The proof tree of the Shallow program (see appendix B, figure 26, upper tree) contains an explicit description of frames and anti-frames. The anti-frames can be found in the pre- and postconditions of the leaves (e.g. \( \text{tree}(p) \)), the frames are indicated in applications of the frame rule (e.g. Frame \( \text{tree}(t) \star \text{tree}(q) \)).

2.5.1 Frames and Anti-frames

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2.5.2 Parallelization

The rewrite rule for parallelization (see figure 19) can be understood as follows: Given a proof of the sequential program \( C \), \( C' \) this proof is rewritten into a proof of the parallel program \( C[C'] \).

The newly obtained proof stays valid because it is a valid instance of the Hoare rules and its leaves are included in the leaves of the input tree [9].

The rewrite rule for parallelization uses the information of frames and anti-frames, for example the postcondition of \( C \) is matched against the frame of \( C' \) (\( \Theta \)).

Application of this rule on Shallow’s proof tree (see figure 15) is shown in figure 26 (appendix B, third and fourth tree). The upper postcondition of the left frame’s update \( \text{tree}(p) \) of Seq \( \text{tree}(p) \) is equal to the frame in the right node of Seq. Also the upper precondition of the right frame’s mirror \( \text{tree}(t) \) update \( \text{tree}(q) \) of Seq \( \text{tree}(t) \star \text{tree}(q) \) is equal to the frame in the left node of Seq. This implies that the left and right node of Seq can be parallelized. The rewritten tree is depicted in the last tree of (see appendix B, figure 26).

2.5.3 Common frames

Every leaf in a proof tree is preceded by an application of frame (see section 2.5.1). When multiple commands are present, redun-
dancy in frames can appear. This redundancy should be removed for the rewrite rules to work [9].

For example, consider the rewrite rule for parallelization (see figure 19): C’s postcondition is matched against C’s frame (Θ). When C’s frame contains more elements (i.e. Θ ∗ ζ) the matching fails. Even though C and C’ can be parallelized.

A solution is to move the redundant frame ζ to a separate frame-node by applying a separate frame rule, this solution is implemented by the rewrite rule illustrated in figure 20. This rule means that when ζ is present in both frames of the input tree, ζ is moved to a separate application of the frame rule at the root in the output tree [9].

```
(Ξ | C(Θ))                                    (Ξ | C(Θ))
|Ξ ∗ Ζ | C(Θ ∗ Ζ | ζ)                      |Θ ∗ Ζ | Ζ | C(Θ ∗ Ζ | ζ)  
Fr                                      Fr
Ξ ∗ Ζ | Ζ | C(Θ ∗ Ζ | ζ)                                Seq
|Factorize
Ξ ∗ Ζ | Ζ | C(Θ ∗ Ζ | ζ)                                    Fr
Ξ ∗ Ζ | Ζ | C(Θ ∗ Ζ | ζ)                                Fr
Ξ ∗ Ζ | Ζ | C(Θ ∗ Ζ | ζ)                                Fr
```

Figure 19: Rewrite rule for parallelization

Figure 20: Rewrite rule for removing redundancy

An example of this redundancy can be found in the proof tree of the Shallow program (see appendix B, figure 26, first tree). This tree contains the frames \(\text{tree}(q) \star \text{tree}(p)\) (in the center) and \(\text{tree}(p) \star \text{tree}(t)\) (on the right), these frames share \(\text{tree}(p)\)\(^2\). Consequently \(\text{tree}(p)\) is moved to a separate frame-node by applying the rewrite rule for factorization (see figure 26, appendix B, second tree).

3. FINDINGS

The rewrite rules for factorization and parallelization, described by Hurlin, have several limitations [9]. This paper proposes an extension to support conditional annotation of programs. Shallow (see section 2.4.3) is an example where the proposed solution solves this problem.

Smallfoot’s annotation includes conditional statements described as:

\[
\text{if } b \text{ then } \Phi_a \text{ else } \Phi_b
\]

The approach based on Hurlin’s work, does not handle such formulas.

As an example, consider a program C, where \(\Xi\) describes C’s precondition; and if \(b\) then \(\Phi_a\) else \(\Phi_b\) describes C’s postcondition. Then, \(\Phi_a\) describes C’s postcondition when \(b\) is true; and \(\Phi_b\) describes C’s postcondition when \(b\) is false.

\(^2\text{Note that } \text{tree}(p)\text{ is also the anti-frame of } \text{update}(p)\) (see section 2.5.1).

Conditional statements in postconditions are handled by Smallfoot’s verifier using a case split:

\[
P \quad P'
\]

This rule means that \(\{\Xi | C(\Theta)\}\) can be proven either by \(P\) or by \(P'\).

The relation between conditional statements in postconditions and frames is shown in figure 25 (appendix B). C’s postcondition (Φ) induces a split case on C’s frame. C’s frame is either \(\Phi_a\) (in the center) or \(\Phi_b\) (on the right).

The proof tree (see appendix B, figure 26, first tree, on the right) of the Shallow program, contains such a split case on the frame of \(\text{update}(q)\). This split case is induced by \textit{mirror}(t, d)’s conditional postcondition:

\[
\text{if } d = 0 \text{ then emp else tree}(t)
\]

The generated proof describes that \(\text{update}(q)\) is framed conditionally by either \(\text{tree}(p)\) or \(\text{tree}(p) \star \text{tree}(q)\). Note that \textit{mirror}(t, d)’s and \(\text{update}(q)\)’s common frame is \(\text{tree}(p)\). This common frame should be factorized before other rewrite rules can be used.

Current rewrite rules do not match proof trees containing conditional statements and split cases. The shape of current rules (e.g. figure 20) describe proof trees containing only applications of sequence and frame rules, thus excluding proof trees having split cases. Éterlou’s rewrite engine stops when split cases are found.

4. SOLUTION

There are two rewrite rules responsible for automatic parallelization: Factorize, which prepares the proof tree by removing common frames and Parallelize, the actual parallelization rewrite rule [9]. Both rewrite rules need to be extended for the procedure to work with conditional statements (see section 3).

4.1 Factorization

There are two possible locations where an split case (see appendix B, figure 25) can be factorized.

1. at the root of the split case’s application, this shall remove the common frame \(\zeta\) of \(\Phi_a\) and \(\Phi_b\), followed by a second factorization at the root of the sequence-node, removing the common frame of \(\Xi_f\) and \(\zeta\) (see appendix C, figure 27).

2. at the root of the sequential’s application, this shall remove the common frame \(\zeta\) of \(\Xi_f\), \(\Phi_a\) and \(\Phi_b\) in a single pass (see figure 21).

The first type of factorization involves a smaller part of the proof tree than the second one. When choosing for simplicity type one is preferable. Note that \(\zeta\) and \(\Xi_f\) might not share a common frame \(\zeta\). In that case a second factorization cannot be applied, which results in an unnecessary frame-node \(\zeta\).

The second type will factorize the three frames \((\Xi_f, \Phi_a, \Phi_b)\) at once. In the case the three trees share a common frame \(\zeta\), the resulting proof tree will have four frames (against five frames in the case of successful application of type one). More importantly, type two does include split cases containing a possible continuation (sequences will be explained in section 4.2).

Because factorization of type two is more general this rewrite rule is preferable. Besides, other rewrite rules will become smaller
Parallelization of conditional statements is supported and the program can be automatically rewritten to parallelize it. Two approaches for factorization of type two have been described, and the program can be factorized as follows:

\[
\begin{align*}
\text{ParallelizeOr} & : (\exists C' \mid C, C' \mid \Phi \to \Phi) \\
\end{align*}
\]

Guard: \( \beta = \text{if } b \text{ then } \Phi_a \text{ else } \Phi_b \)

Figure 22: Rewrite rule for parallelization including split cases

As an example Shallow’s proof tree (see section 2.4.3) is parallelized by applying ParallelizeOr (see appendix A, figure 24) on Shallow’s factorized proof tree (see appendix B, figure 26, second tree). The resulting tree shows that mirror\((t, d)\) and update\((q)\) are parallelized. In this case the guards are instantiated as follows:

\[
\begin{align*}
\Theta' & \equiv \tau(q) \quad \text{The anti-frame of } \text{update}(q) \text{ is equal to the frame of } \text{mirror}(t, d) \\
\Phi_a & \equiv \text{emp} \quad \text{The frame of the left leaf of the split case is equal to the first part of the conditional anti-frame } (\Phi) \text{ of } \text{mirror}(t, d) \\
\Phi_b & \equiv \tau(t) \quad \text{The frame of the right leaf of the split case } (\Phi_b) \text{ is equal to the second part of the conditional anti-frame } (\Phi) \text{ of } \text{mirror}(t, d)
\end{align*}
\]

One can note that ParallelizeOr is not able to parallelize \text{update}(p) and \text{mirror}(t, d); this is done in the last rewrite step using the Parallelize rule (see figure 19).

In practise should the proposed rewrite rule for parallelization be applied after the rule for factorization of type two. Using the rule for factorization of type one is cumbersome, because it requires to write a dedicated parallelization rule (to match frame \(\zeta\)). Hence, factorization of type two is preferable.

5. CONCLUSION

The mechanism proposed by Hurlin where separation logic is used for automatic parallelization has several limitations. This paper addressed one of these limitations: we allow the usage of conditional statements in annotations.

With the proposed changes a specification’s proof tree containing conditional statements is supported and the program can be automatically rewritten to parallelize it. Two approaches for factorization of conditional statements have been described, and the factorization of type two is identified as the preferable one.

Shallow, an example program, has been used to illustrate this paper findings and proposed solution. This solution has been implemented in the etelou tool and tested for soundness. There are other rules than factorization and parallelization and they might have to be changed to allow conditional statements. This is left open for further research.

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REFERENCES


APPENDIX A: REWRITE RULES

Figure 23: Rewrite rule to factorize applications of frame and split case

Guard: $\Xi_f \Leftarrow \Xi_f \circ \Xi$ and $\Theta_{/u} \Leftarrow \Xi_f \circ \Xi$ and $\Theta_{/h} \Leftarrow \Xi_f \circ \Xi$

Figure 24: Rewrite rule to parallelize applications of frame and split case

Guard: $\Theta \Rightarrow \text{if } \beta \text{ then } \Phi_{/h} \text{ else } \Phi_{/h} ; \Phi_{/h} \Rightarrow \beta \text{ } \Phi_{/h}$ and $\Theta_{/h} \Rightarrow \beta \text{ } \Phi_{/h}$
APPENDIX B: PROOF TREES

\[ \text{Frame } \Theta_{\mathcal{F}} \]
\[ |\Theta_{\mathcal{F}} \subseteq \mathcal{F} \]
\[ \text{Seq } \]
\[ |\Phi \]
\[ \text{Or } \]
\[ |\mathcal{F} \]
\[ |\mathcal{C} \]
\[ |\mathcal{C} \]

\[ \Phi \equiv \text{ if } \beta \text{ then } \Phi_a \text{ else } \Phi_b \]

Figure 25: Shape of a split case

\[ \text{FactorizeOrFrames} \]
\[ \text{ParallelizeOr} \]
\[ \Phi \equiv \text{ if } d = 0 \text{ then } \text{emp} \text{ else } t(i) \]

\[ t(r) \equiv \text{tree}(r) \]
\[ \text{Fr } \mathcal{F} \equiv \text{Frame } \mathcal{F} \]
\[ f_i \equiv \text{update}(p_i) \]
\[ f_j \equiv \text{mirror}(t, d) \]
\[ f_i \equiv \text{update}(q_i) \]

Figure 26: Proofs of the Shallow program.
APPENDIX C: AN ALTERNATIVE FOR THE FACTORIZATION REWRITE RULE

![Rewrite rule diagram](image_url)

Figure 27: Rewrite rule for removing redundancy including split cases, type one